Preliminary

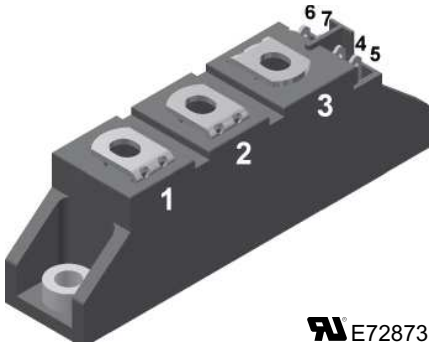
Dual Power MOSFET Module

V_{DSS} = 70 V
I_{D25} = 165 A
$R_{DS(on)}$ = 7 mΩ

Common-Source connected
N-Channel Enhancement Mode

Part number

VMK165-007T



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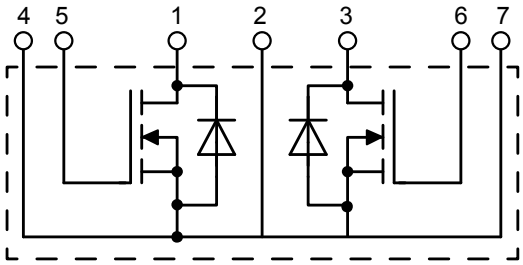


Features / Advantages:

- Two MOSFET with common source
- Direct copper bonded Al_2O_3 ceramic base plate
- Low $R_{DS(on)}$ HDMOS™ process
- Low package inductance for high speed switching
- Kelvin source contact
- Keyed twin plugs
- High power density
- Low losses

Applications:

- Push-pull inverters
- Switched-mode and resonant-mode power supplies
- Uninterruptible power supplies (UPS)
- AC static switches

Package: TO-240AA

- Isolation Voltage: 4800 V~
- Industry standard outline
- RoHS compliant
- Soldering pins for PCB mounting
- Base plate: DCB ceramic
- Reduced weight
- Advanced power cycling

Preliminary

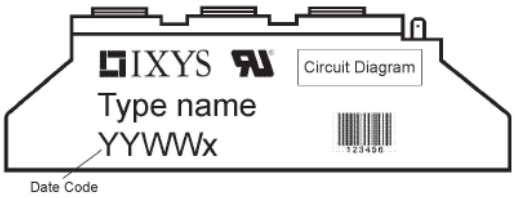
MOSFETs			Ratings			
Symbol	Definitions	Conditions	min.	typ.	max.	Unit
V_{DSS}	*drain source breakdown voltage*	T_{VJ} = 25°C to125°C			70	V
V_{DGR}	*drain gate voltage*	R_{GS} = 6.8 kΩ T_{VJ} = 25°C to125°C			70	V
V_{GS}	*gate source voltage*	Continuous			±20	V
V_{GSM}	*max. transient gate source voltage*	Transient			±30	V
I_{D25}	*continuous drain current*	T_C = 25°C			165	A
I_{D100}	*drain current*	T_C = 100°C			104	A
I_{DM}	*maximum pulsed drain current*	t_p = 10 µs, pulse width limited by T_{JM} T_C = 25°C			660	A
P_{tot}	*total power dissipation*	T_{VJ} = 125°C T_C = 25°C			390	W
V_{DSS}	*drain source breakdown voltage*	V_{GS} = 0 V; I_D = 1 mA	70			V
$V_{GS(th)}$	*gate threshold voltage*	V_{DS} = V_{GS}; I_D = 8 mA	2		4	V
I_{GSS}	*gate source leakage current*	V_{GS} = ±20 V DC; V_{DS} = 0			500	nA
I_{DSS}	*drain source leakage current*	V_{DS} = V_{DSS}; V_{GS} = 0 V T_{VJ} = 25°C			200	µA
		V_{DS} = 0.8 • V_{DSS}; V_{GS} = 0 V T_{VJ} = 125°C			1	mA
$R_{DS(on)}$	*staticdrain source on resistance*	V_{GS} = 10 V; I_D = 0.5 • I_{D25} T_{VJ} = 25°C Pulse test, t ≤ 300 µs, duty cycle d ≤ 2 %		6	7	mΩ
g_{fs}	*forward transconductance*	V_{DS} = 10 V; I_D = 0.5 • I_{D25} pulsed	60	80		S
C_{iss}	*input capacitance*	V_{GS} = 0 V; V_{DS} = 25 V; f = 1 MHz		8.8		nF
C_{oss}	*output capacitance*			4.0		nF
C_{rss}	*reverse transfer (Miller) capacitance*			2.4		nF
$t_{d(on)}$	*turn-on delay time*	V_{GS} = 10 V; V_{DS} = 0.5 • V_{DSS}; I_D = 0.5 • I_{D25} R_G = 1 Ω (external), resistive load		120		ns
t_r	*current rise time*			280		ns
$t_{d(off)}$	*turn-off delay time*			390		ns
t_f	*current fall time*			110		ns
Q_g	*total gate charge*	V_{GS} = 10 V; V_{DS} = 0.5 • V_{DSS}; I_D = 0.5 • I_{D25}		480		nC
Q_{gs}	*gate source charge*			60		nC
Q_{gd}	*gate drain (Miller) charge*			240		nC
R_{thJC}	*thermal resistance junction to case*				0.32	K/W
R_{thJH}	*thermal resistance junction to heatsink*	with heat transfer paste		0.2		K/W

Source-Drain Diodes			Ratings			
Symbol	Definitions	Conditions	min.	typ.	max.	
I_S	*continuous source current*	V_{GS} = 0 V			165	A
I_{SM}	*maximum pulsed source current*	Repetitive; pulse width limited by T_{JM}			660	A
V_{SD}	*forward voltage drop*	I_F = I_S; V_{GS} = 0 V Pulse test, t ≤ 300 µs, duty cycle d ≤ 2 %			1.5	V
t_{rr}	*reverse recovery time*	I_F = 50 A, -di/dt = 200 A/µs; V_{DS} = 25 V; V_{GS} = 0 V		150		ns

Data according to IEC 60747 and refer to a single thyristor/diode unless otherwise stated. T_J = 25°C, unless otherwise specified

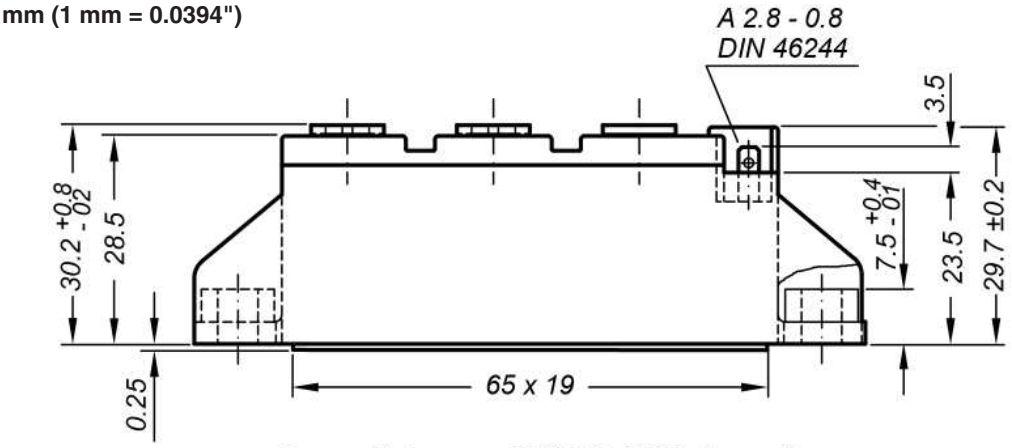
Preliminary

Package	TO-240AA				Ratings			
Symbol	Definitions	Conditions			min.	typ.	max.	Unit
I_{RMS}	*RMS current*	per terminal					200	A
T_{VJ}	*virtual junction temperature*				-40		150	°C
T_{VJM}	*maximum virtual junction temperature*						150	°C
T_{stg}	*storage temperature*				-40		125	°C
Weight						81		g
M_D	*mounting torque*				2.5		4	Nm
M_T	*terminal torque*				2.5		4	Nm
$d_{Spp/App}$	*creepage distance on surface / striking distance through air*		*terminal to terminal*	13.0	9.7			mm
$d_{Spb/Apb}$			*terminal to backside*	16.0	16.0			mm
V_{ISOL}	*isolation voltage*	t = 1 second	50/60 Hz, RMS, $I_{ISOL} \leq 1$ mA		4800			V
		t = 1 minute			4000			V

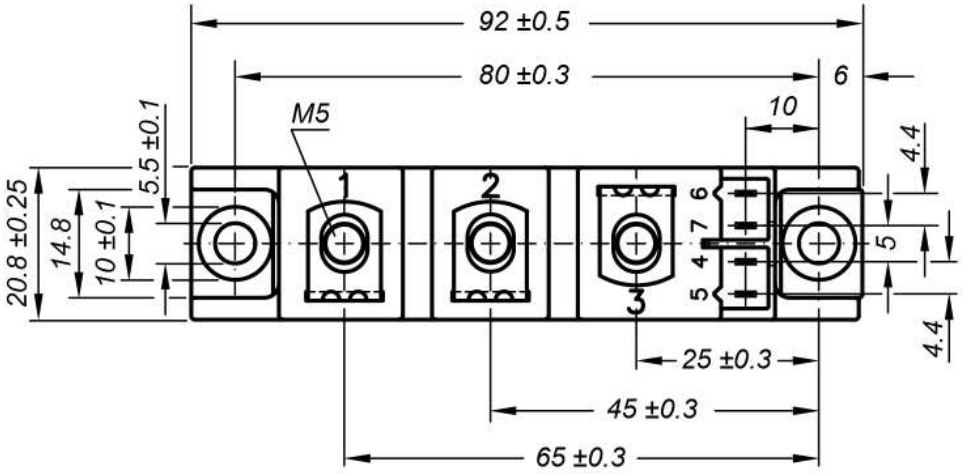


Outlines TO-240AA

Dimensions in mm (1 mm = 0.0394")



General tolerance: DIN ISO 2768 class „c“



Optional accessories for modules
Keyed gate/cathode twin plugs with wire length = 350 mm, gate = white, cathode = red
Type ZY 200L (L = Left for pin pair 4/5)
Type ZY 200R (R = Right for pin pair 6/7) } UL 758, style 3751